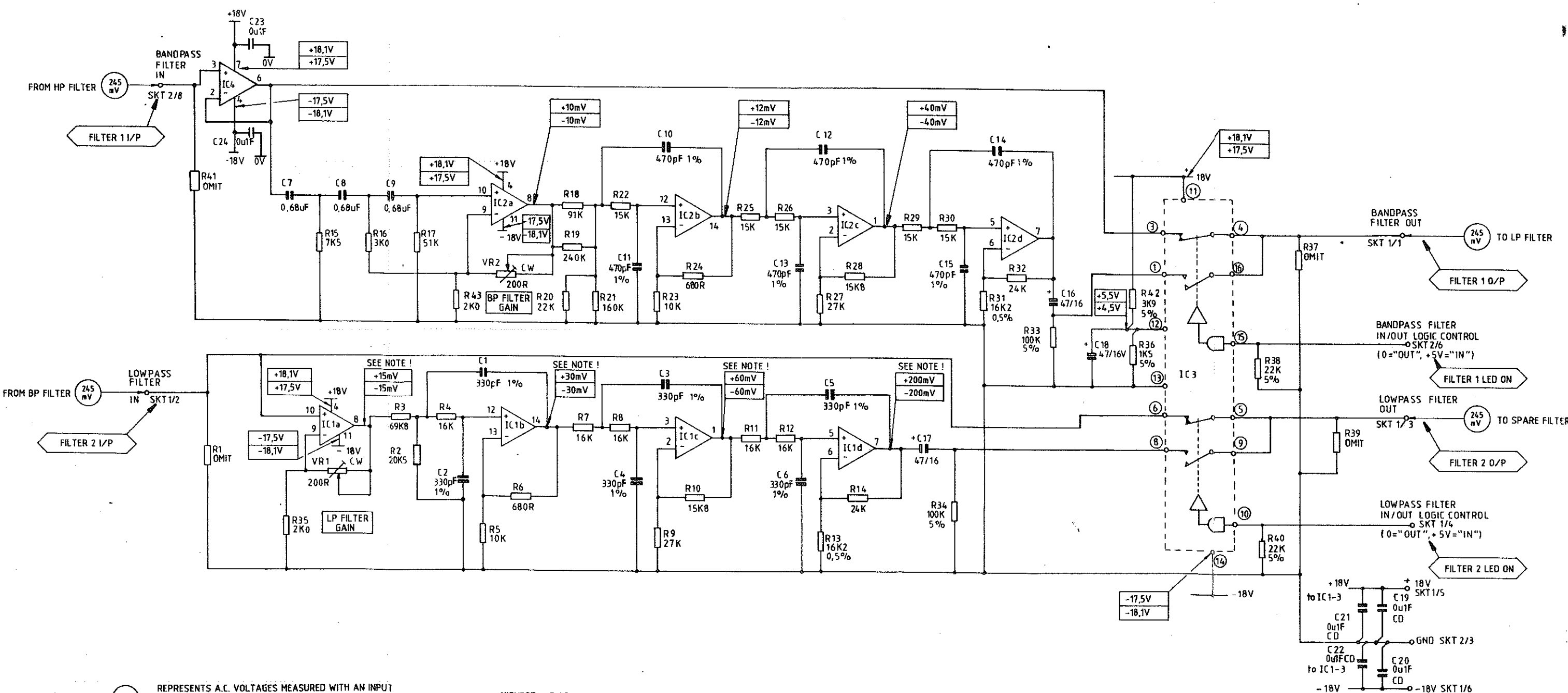


REVISION			
NO	DATE	CHANGE	APPRO. I.D.
1	5-7-83	IC4 ADDED, R4.1 WAS 100K, R1 REMOVED, R3 WAS 15K	
2	21-12-84	SOCKET NO CHANGED	
3	12.1.84	R2 WAS 10K, R3 WAS 84K5	



REPRESENTS A.C. VOLTAGES MEASURED WITH AN INPUT IMPEDANCE OF 10K OHMS OR GREATER. UNLESS OTHERWISE SPECIFIED THESE LEVELS CORRESPOND TO A 1KHz TONE AT A REFERENCE LEVEL OF 0dBu (0.775V) INTO A FULLY CALIBRATED TEST SET, 'LEVEL' FUNCTION, 0dB RANGE

REPRESENTS D.C. VOLTAGES MEASURED WITH A VOLTMETER OF 20,000Ω/V OR GREATER. MAXIMUM IS DEFINED AS THE MOST POSITIVE VOLTAGE WHEN MEASURING VOLTAGES AT IC1 PINS 8, 14, 1 OR 7. SUBTRACT ANY VOLTAGE APPEARING AT IC1 PIN 10 BEFORE COMPARING WITH THESE LIMITS.

EXTENDER TEST POINT OR INDICATOR REFERENCE

USE EXTENDER CARD PCB 75032

HIGHEST R 43
C 24
IC 4
VR 2

IC1 & IC2 = LF347 OR uAF774

IC3=DG243, IC4=LF356

ALL UNMARKED RESISTORS 1% 1/4 W

NEUTRIK AG		TITLE PCB 75024	
Unit 2, Samuel Whites Industrial Estate, Medina Road, Cowes, Isle of Wight, PO31 7LP GB Tel: (0983) 299959 Telefax: (0983) 290142		BANDPASS / LOWPASS FILTER CARD CIRCUIT DIAGRAM	
TOLERANCES - UNLESS OTHERWISE STATED Linear Millimetres: 2.0-4.0mm Angular Degrees: ±0.25°		USE MJS401D, TP401, TT402	
MATERIAL	ORIGINAL SCALE	DRAWN D. CHEN	DRAWING NO
DATE 9/2/83	CHECKED M. J. S.	PROJECT NO 750	TYPE C
DATE	DATE	SHEET NO 132	SIZE 1
		REV 3	

75024/132/1